

FIG. 1 is a block diagram of a system architecture.

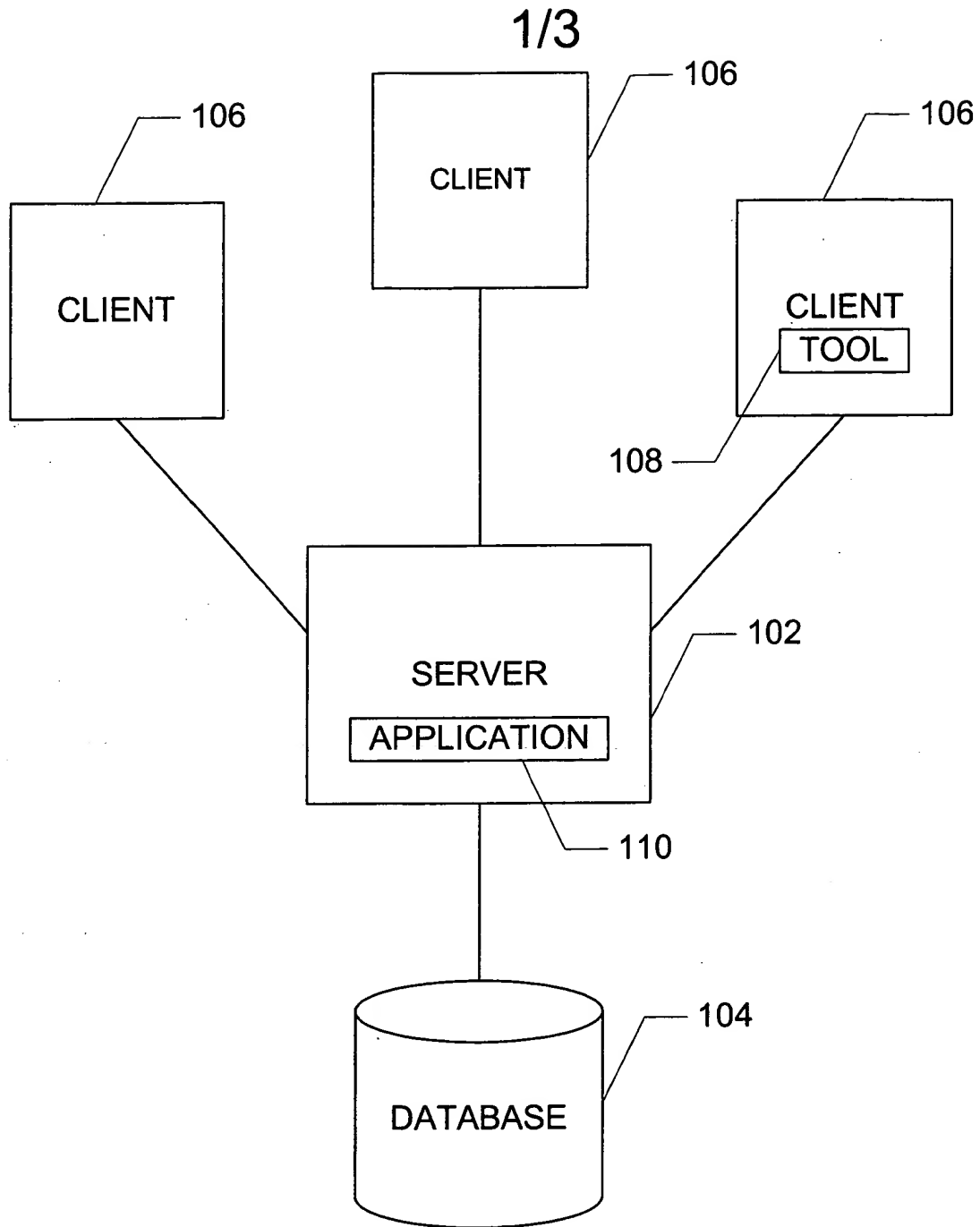


FIG. 1  
Prior Art

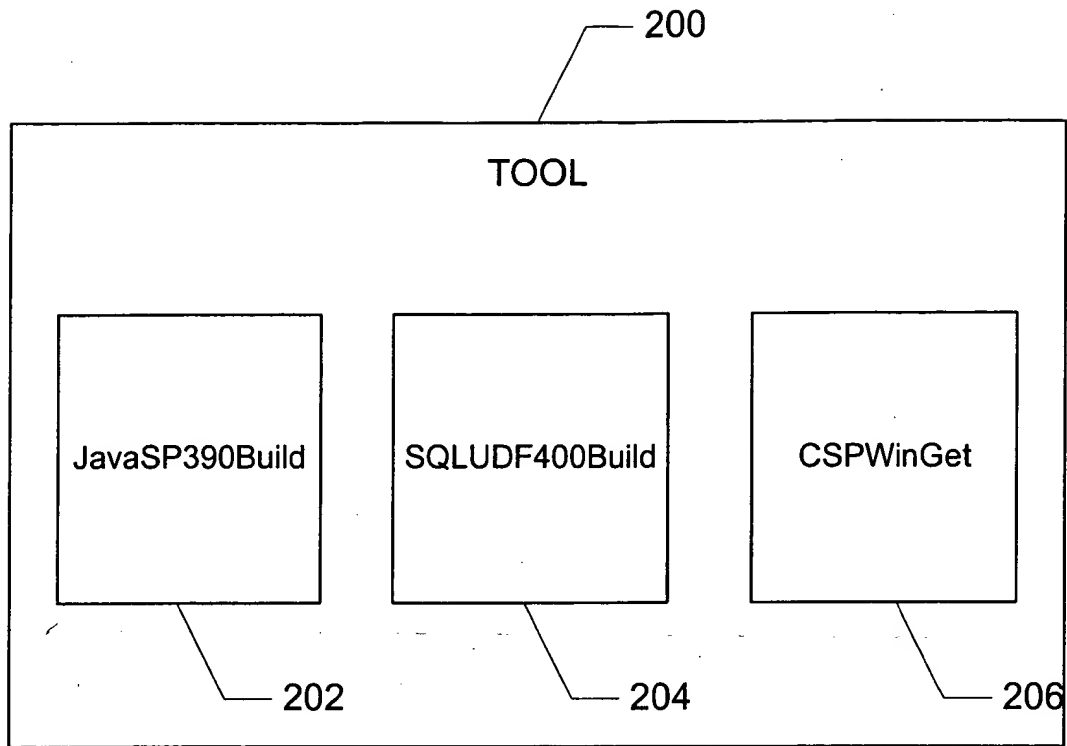


FIG. 2

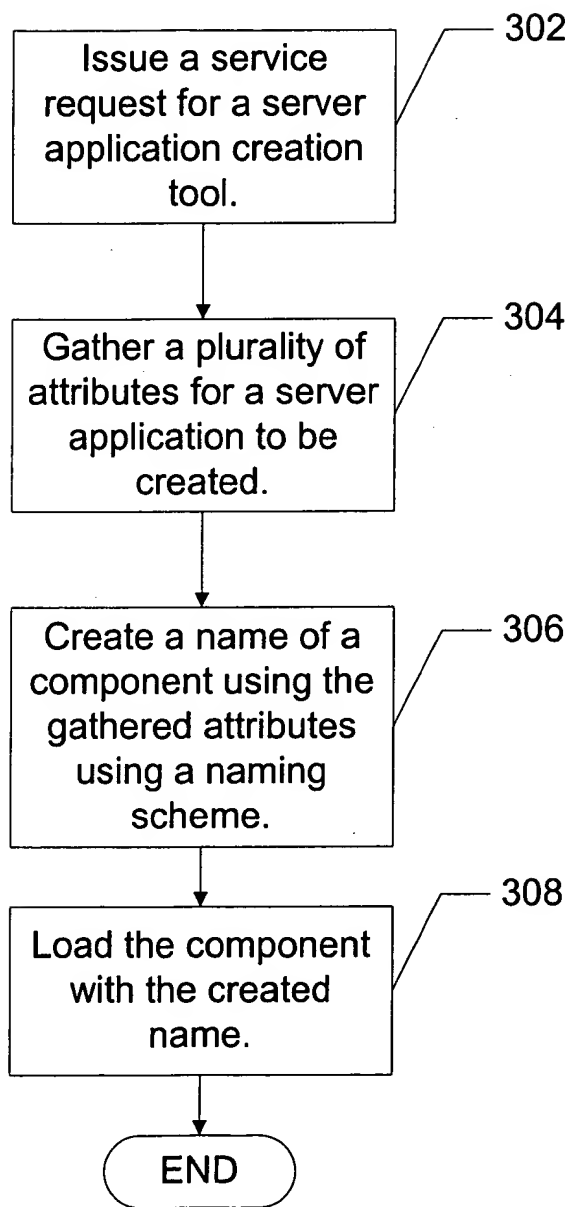


FIG. 3